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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Brian G. Johnson

Serial No.: 10/633,874

Filed: August 4, 2003

For: Lateral Phase Change
Memory

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Art Unit: 2826

Examiner: Remmon R. Forde

Docket: ITO.0047US
P16202

Assignee: Intel Corporation

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Please enter
the attached
amendment
TD

REPLY TO FINAL REJECTION

Sir:

8/11/05

In response to the final rejection mailed June 30, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: July 13, 2005

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Monica Jacobs